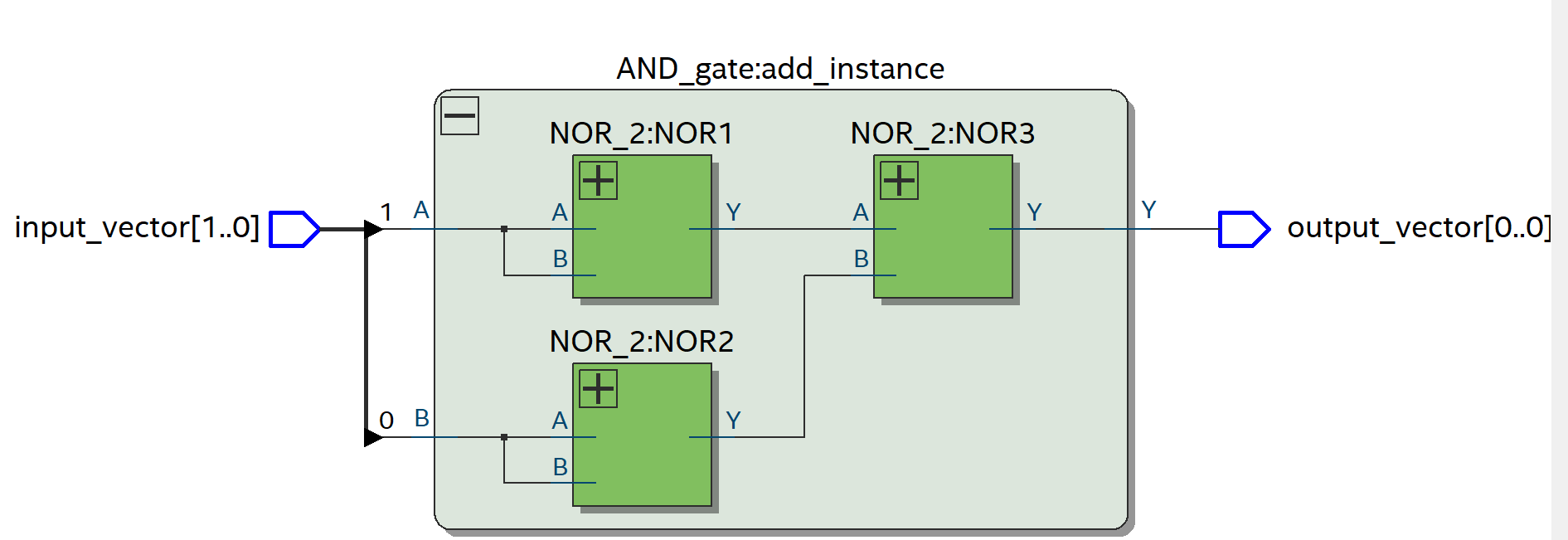
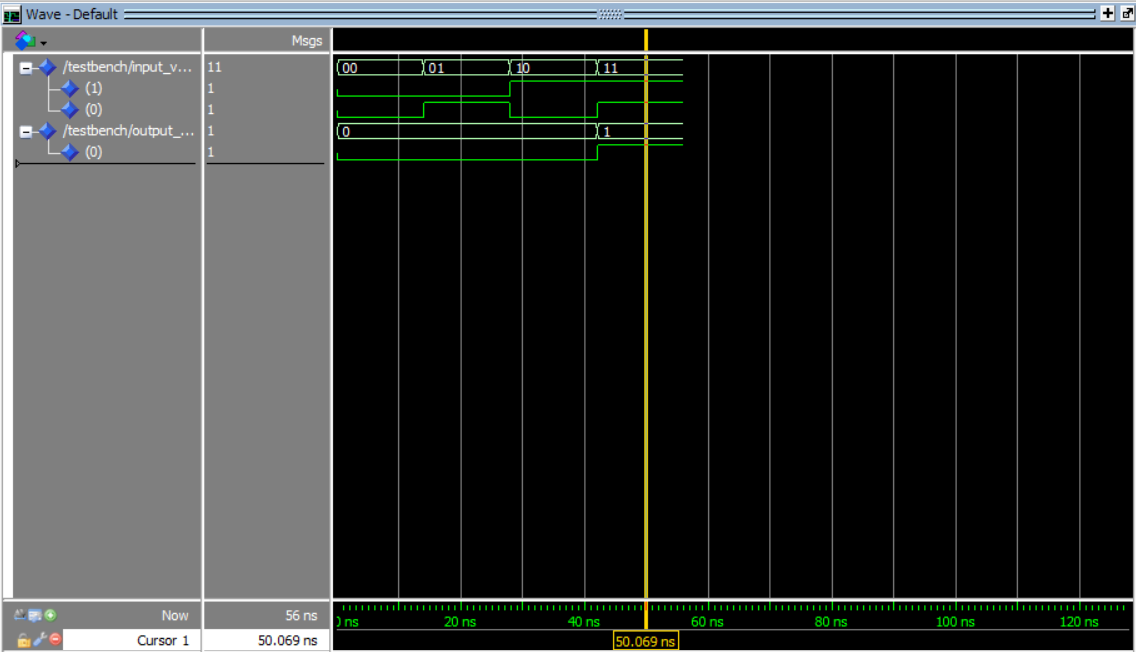
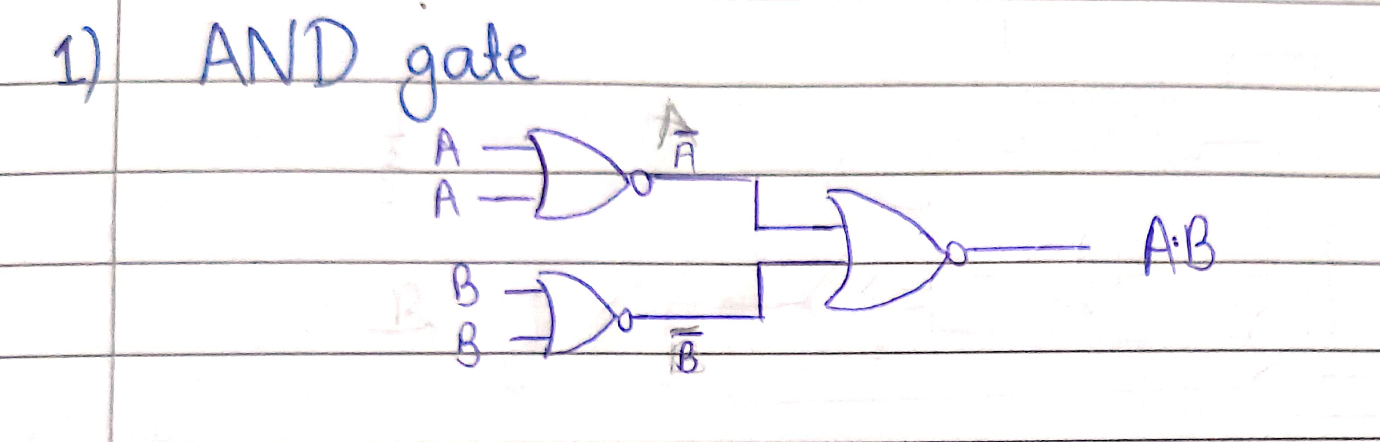
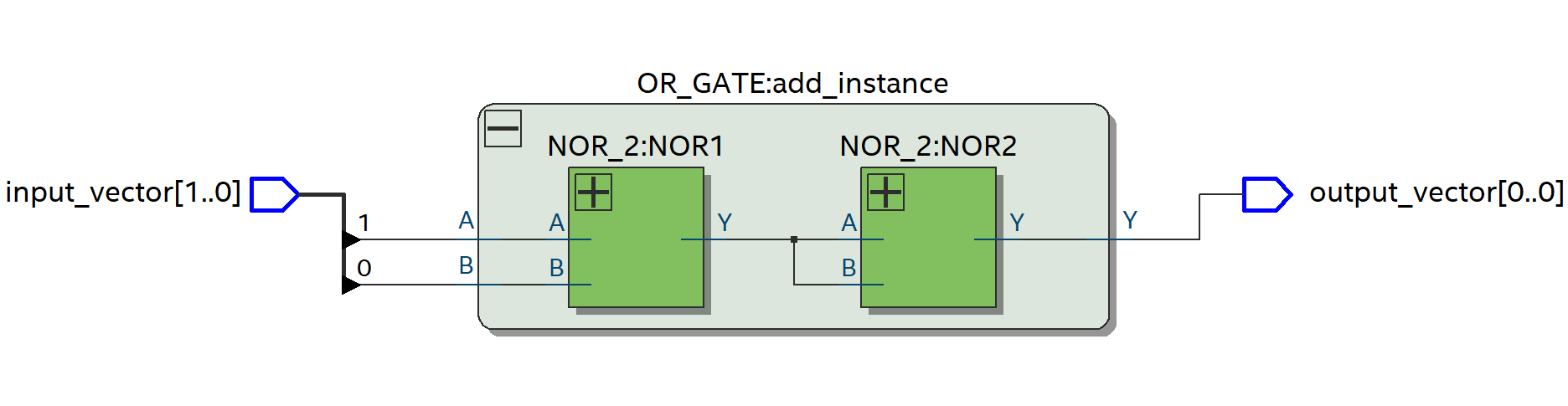
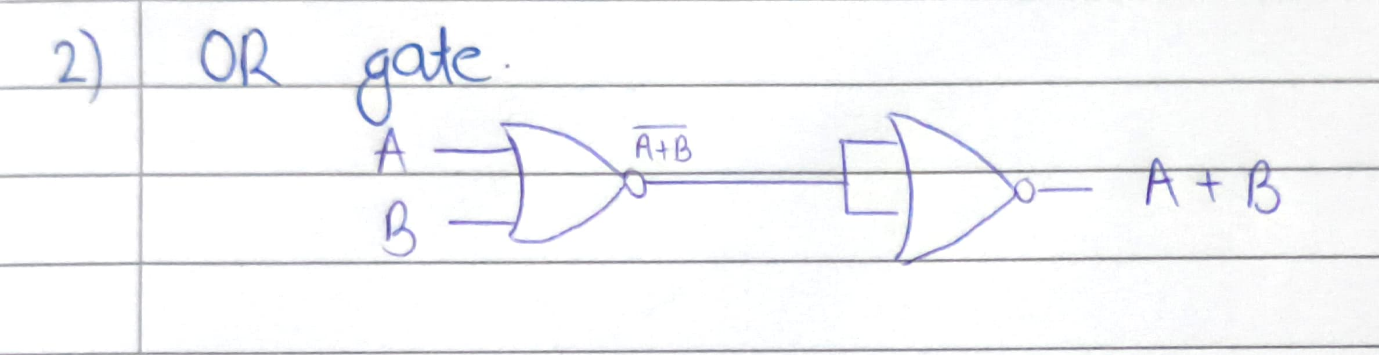
**EE214 Digital Circuits Laboratory**

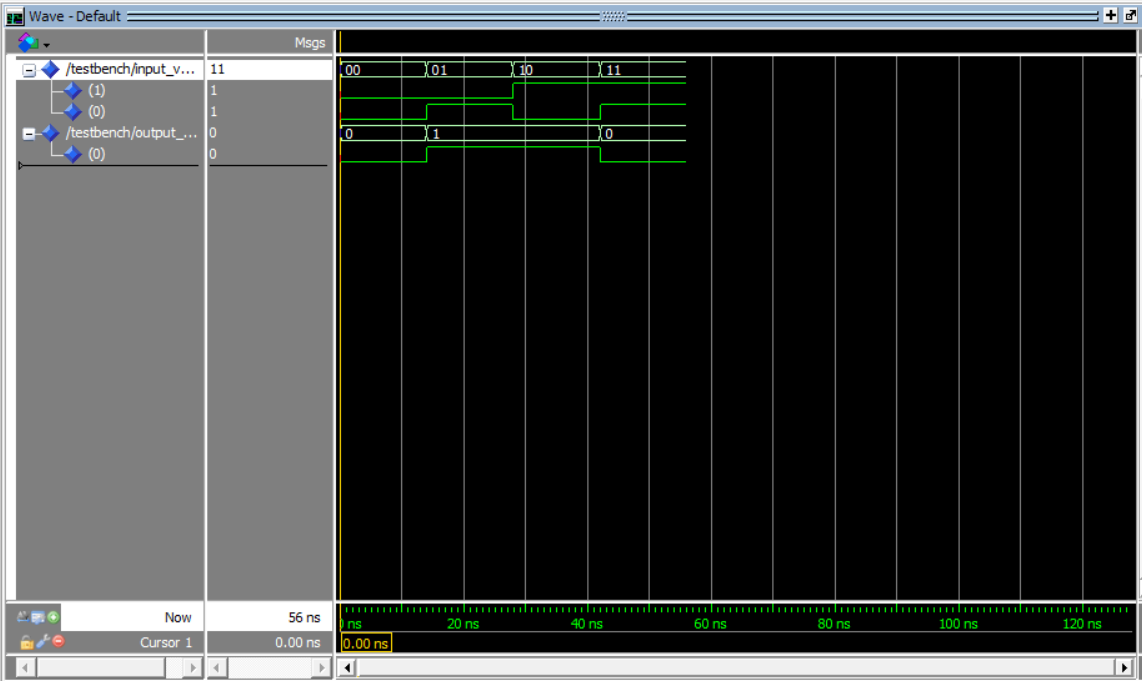
EXP1 - Designing Circuits using Universal Gate NOR

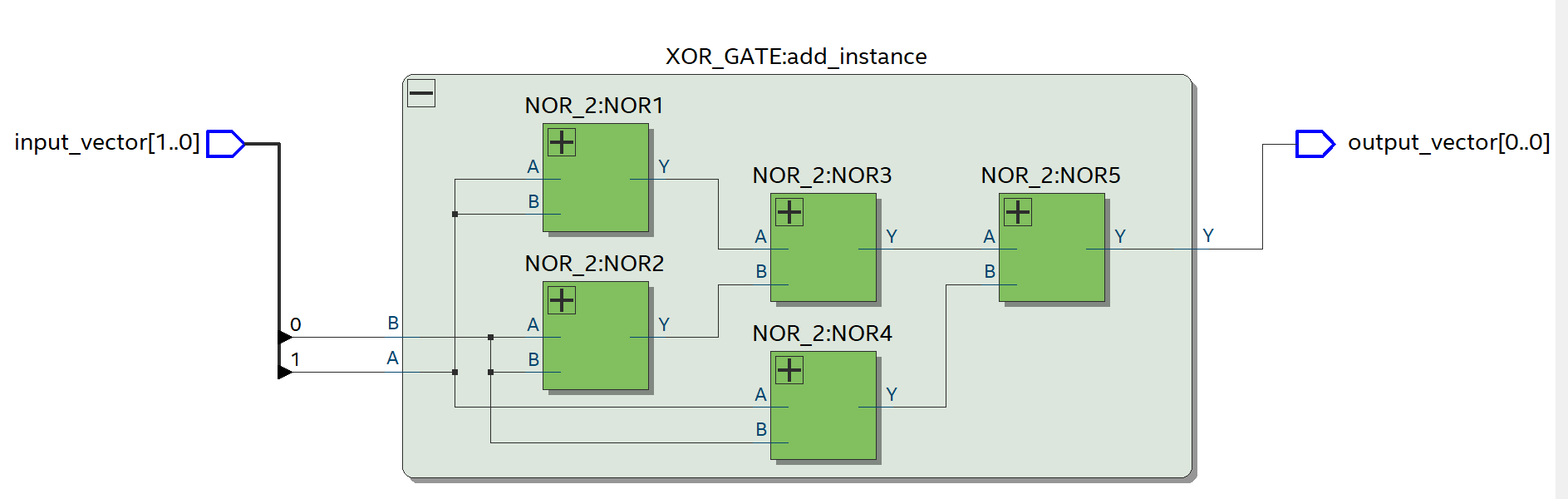
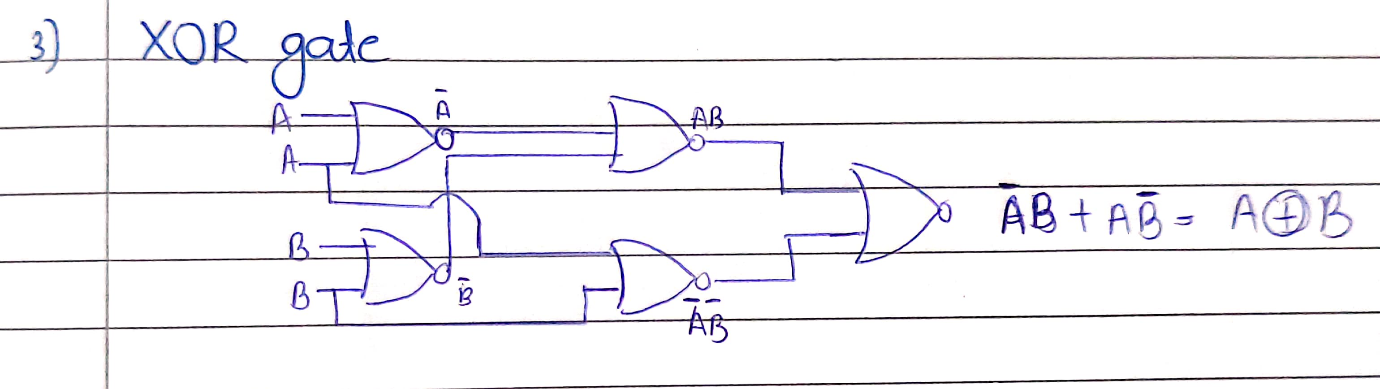
AND gate

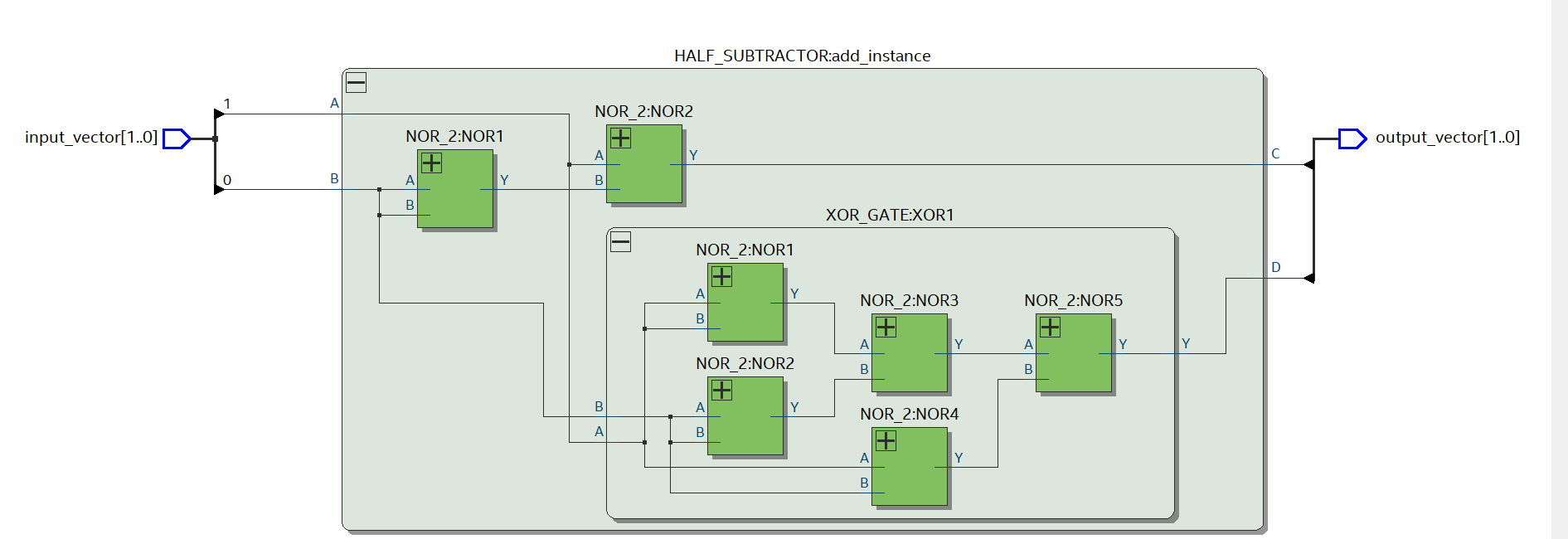


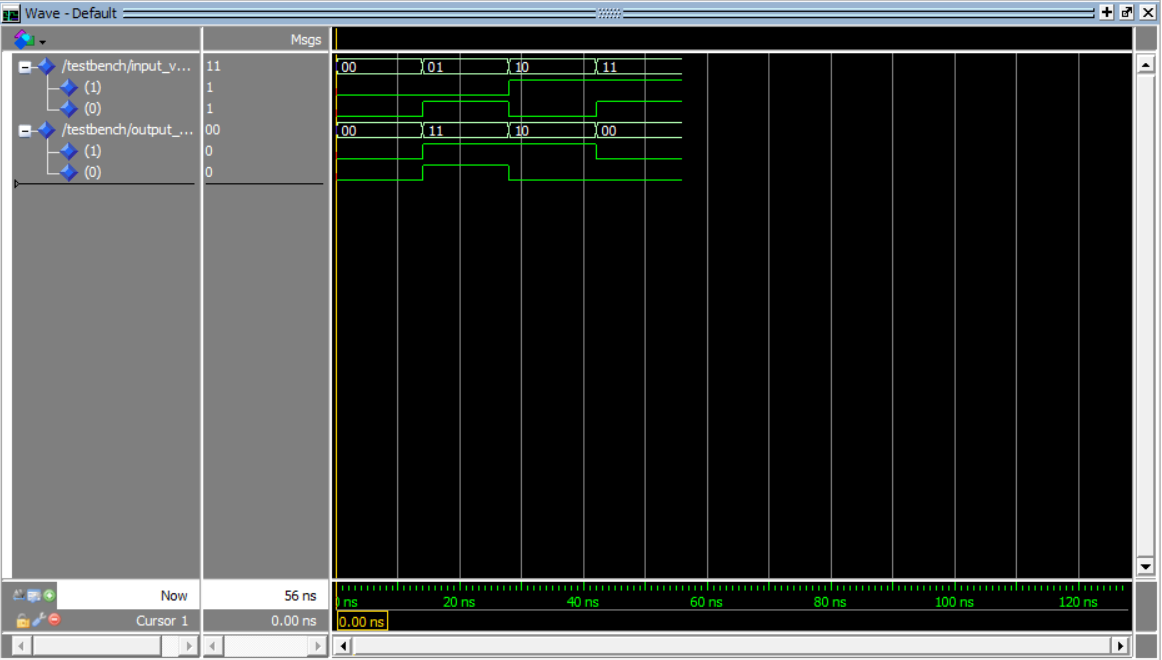
OR gate

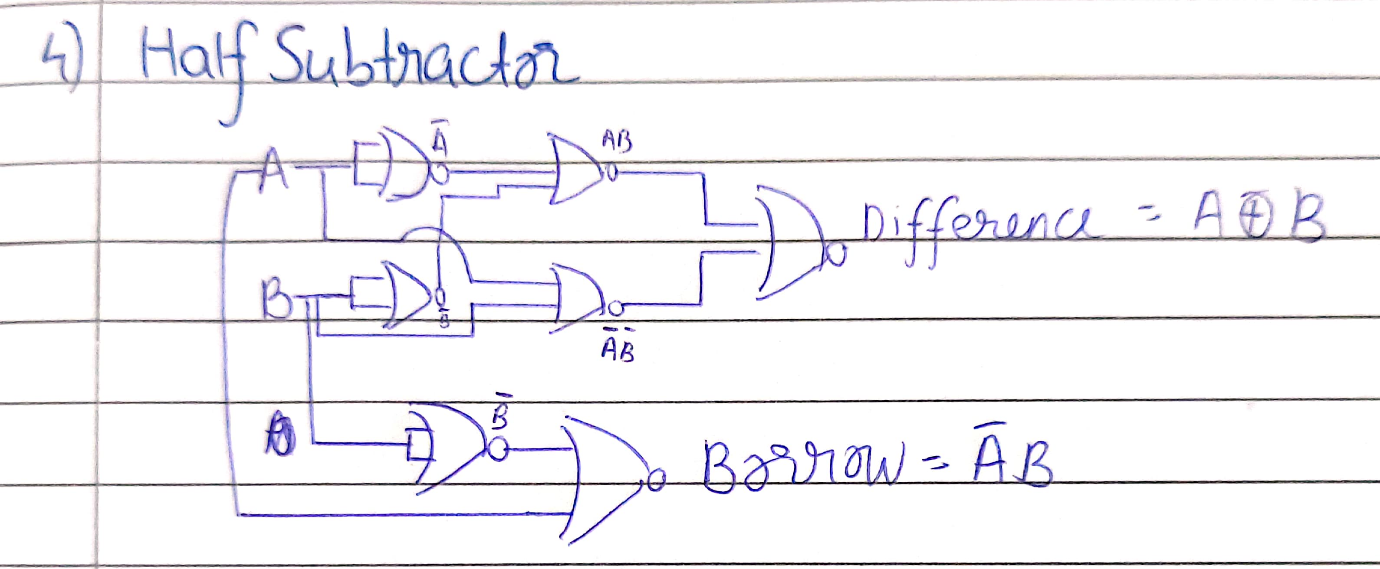


XOR gate



Half Subtractor





Full Subtractor

